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Patent 6,746,893PATENTIN UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No.: 6,746,893 b1

Docket No: 303.324US2

Issue Date: June 8, 2004

Patentee: Leonard Forbes et al.

Customer No.: 21186

Confirmation No.: 1086

Title TRANSISTOR WITH VARIABLE ELECTRON AFFINITY GATE AND  
METHODS OF FABRICATION AND USEREQUEST FOR CERTIFICATION OF CORRECTIONCommissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450Certificate

AUG 03 2004

of Correction

It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. Two copies of the text of the Certificate in the suggested form are enclosed.

Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims as properly allowed, and re-examination is not required.

**As the error is that of the Patent Office, it is believed that no fee is due.**

The Examiner is authorized to charge any additional fees or credit overpayment to Deposit Account No.19-0743.

Respectfully Submitted

LEONARD FORBES ET AL.

By their Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A  
P.O. Box 2938  
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By:

David R. Cochran  
Reg. No: 46,632  
DRC:kerDate: 21 July 2004David R. Cochran

CERTIFICATE UNDER 37 CFR § 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450, on this 26 day of July 2004

Name

Kirstin Ryan

Signature

Kirstin Ryan

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## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 6,746,893 *B1*

DATED : June 8, 2004

INVENTOR(S) : Forbes, et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On page 2, in field (56), under "Foreign Patent Documents", in column 2, line 16, after "8/1995" insert - - H01L/29/78 - -.

On page 3, in field (56), under "Other Publications", in column 1, line 1, delete "Application" and insert - - Applications - -, therefor.

On page 3, in field (56), under "Other Publications", in column 1, line 17, delete "112" and insert - - 132 - -, therefor.

On page 3, in field (56), under "Other Publications", in column 1, line 35, delete "PEVD" and insert - - PECVD - -, therefor.

On page 3, in field (56), under "Other Publications", in column 1, line 56, delete "79" and insert - - 69 - -, therefor.

On page 3, in field (56), under "Other Publications", in column 2, line 14, delete "Amorphic" and insert - - Amorphous - -, therefor.

On page 4, in field (56), under "Other Publications", in column 1, line 38, delete "525-524, (May 1994)" and insert - - 515-524, (May 1984) - -, therefor.

On page 5, in field (56), under "Other Publications", in column 1, line 26, delete "Electronic" and insert - - Electronics - -, therefor.

On page 5, in field (56), under "Other Publications", in column 1, line 31, delete "Neuceck" and insert - - Neudeck - -, therefor.

On page 5, in field (56), under "Other Publications", in column 1, line 43, delete "Electronics" and insert - - Electronic - -, therefor.

MAILING ADDRESS OF SENDER:

SCHWEGMAN, LUNDBERG, WOESSNER, & KLUTH, P.A.  
P.O. BOX 2938  
Minneapolis, MN 55402

Atty Docket No: 303.324US2

PATENT NO. 6,746,893 *B1*

No. of additional copies

      

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On page 5, in field (56), under "Other Publications", in column 2, line 27, after "(1987)"  
insert - - ) - -.

In column 1, line 31, delete "e g." and insert - - e.g. - -, therefor.

In column 1, line 58, after "for" delete "in".

In column 3, line 62, delete "erase" and insert - - erasure - -, therefor.

In column 4, line 38, delete "illustrating" and insert - - illustrates - -, therefor.

In column 5, line 16, delete "polycrystalline" and insert - - polycrystalline - -, therefor.

In column 6, line 37, after "thick" insert - - . - -.

In column 7, line 55, delete "programing" and insert - - programming - -, therefor.

In column 8, line 18, delete "polyclinic" and insert - - polycrystalline - -, therefor.

In column 8, line 50, delete "0.4" and insert - - -0.4 - -, therefor.

In column 8, line 65, delete "Microcrystaline" and insert - - Microcrystalline - -, therefor.

In column 9, line 12, delete "carrier" and insert - - barrier - -, therefor.

In column 10, line 2, after "flash" delete ",".

In column 11, line 5, after "erase" insert - - - - -.

In column 11, line 21, after "absorbed" insert - - in - -.

In column 12, line 34, delete "microcrystaline" and insert - - microcrystalline - -, therefor.

In column 12, line 37, after "in" delete ",".

In column 15, line 39, delete "programing" and insert - - programming - -, therefor.

In column 16, line 41, in Claim 12, after "comprises" delete ",".

In column 20, line 28, in Claim 33, delete "boren" and insert - - boron - -, therefor.

In column 22, line 36, in Claim 44, delete "herein" and insert - - wherein - -, therefor.

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Leonard Forbes et al.

Examiner: Trinh, Michael

Patent No.: 6,746,893

Group Art Unit: 2822

Issue Date: June 8, 2004

Docket No: 303.324US2

Title: TRANSISTOR WITH VARIABLE ELECTRON AFFINITY GATE AND METHODS OF  
FABRICATION AND USE

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

We are transmitting herewith the attached:

- Request for Certificate of Correction.  
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 A return postcard.

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SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

Customer No: 21186

By:

Name: David R. Cochran

Reg. No.46,632

DRC:CMG:ker

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